

10. (Amended) Display apparatus comprising :

- a cathode ray tube,
- a first circuit providing a high voltage to the cathode and
- a second circuit receiving a gross signal on a source input and providing on at least an output at least a luminance signal controlling an electron stream of the cathode ray tube,

the second circuit comprising a comparator having an input connected to said source input and generating an error signal according to a difference between said comparator input and a reference signal, and controlled amplifying means for amplifying the gross signal into the luminance signal according to the error signal,

wherein the comparator input is connected to ground through a switch controlled by a signal generated from a microprocessor when the apparatus switches from on to off.

11. Display apparatus according to claim 10, the apparatus being a television receiver.

IN THE ABSTRACT:

Please add the attached Abstract.

REMARKS

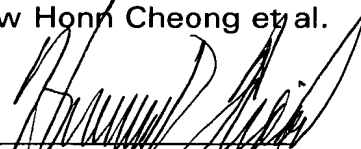
The specification has been amended to include a reference to the priority application.

The above amendments to the claims have been made to eliminate reference indicia and to meet the requirements of the USPTO.

To meet the requirements of the United States, the Abstract has been added.

No fee is believed to have been incurred by virtue of this amendment. However, if a fee is incurred on the basis of this amendment, please charge such fee against deposit account 07-0832.

Respectfully submitted,
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Enclosures

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MARKED UP CLAIMS

1. (Amended) Display apparatus comprising :

- a cathode ray tube [(3)],
- a first circuit [(1)] providing a high voltage [(HV)] to the cathode and
- a second circuit [(2)] receiving a gross signal [(Y')] on a source input and providing on at least an output at least a luminance signal [(Y)] controlling an electron stream of the cathode ray tube [(3)], [characterised by] wherein said
 - means for simulating absence of gross signal [(Y')] when the apparatus switches from on to off.

2. (Amended) Display apparatus according to claim 1, wherein said means for simulating absence of gross signal [(Y')] are triggered by a signal [(SH)] sent by a microprocessor.

3. (Amended) Display apparatus according to claim 1 [or 2], wherein a pin [(42)] carrying a signal representative of the gross signal [(Y')] when the apparatus is on is connected to ground when the apparatus switches from on to off.

4. (Amended) Display apparatus according to claim 1, wherein the second circuit [(2)] comprises a comparator [(20)] having an input [(42)] connected to said source input and generating an error signal [(E)] according to a difference between said comparator input and a reference signal [(Vo)], and controlled amplifying means [(22)] for amplifying the gross signal [(Y')] into the luminance signal [(Y)] according to the error signal [(E)], and wherein a signal simulating absence of gross signal [(Y')] is sent to the comparator input [(42)] when the apparatus switches from on to off.

5. (Amended) Display apparatus according to claim 4, wherein said signal simulating absence of gross signal [(Y')] is controlled by a signal [(SH)] sent by a microprocessor.

5 6. (Amended) Display apparatus according to claim 4 [or 5], wherein the comparator input [(42)] is connected to ground when the apparatus switches from on to off.

10 7. (Amended) Display apparatus according to claim 4, wherein the comparator input [(42)] is connected to ground through a switch [(K)].

8. (Amended) Display apparatus according to claim 7, wherein the switch [(K)] is controlled by a signal [(SH)] from a microprocessor.

15 9. (Amended) Display apparatus according to [any of the preceding claims] claim 1, wherein the apparatus is a television receiver.

10. (Amended) Display apparatus comprising :

20 - a cathode ray tube [(3)],
- a first circuit [(1)] providing a high voltage [(HV)] to the cathode and
- a second circuit [(2)] receiving a gross signal [(Y')] on a source input and providing on at least an output at least a luminance signal [(Y)] controlling an electron stream of the cathode ray tube [(3)],

25 the second circuit [(2)] comprising a comparator [(20)] having an input [(42)] connected to said source input and generating an error signal [(E)] according to a difference between said comparator input and a reference signal [(Vo)], and controlled amplifying means [(22)] for amplifying the gross signal [(Y')] into the luminance signal [(Y)] according to the error signal [(E)],

30 [characterised in that] wherein the comparator input [(42)] is connected to ground through a switch [(K)] controlled by a signal [(SH)] generated from a microprocessor when the apparatus switches from on to off.

11. Display apparatus according to claim 10, the apparatus being a television receiver.

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